



PATENT  
Attorney Docket No. 211178  
Client Reference No. F32292US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Michael J. UNDERHILL

Application No. 09/831,413

Filed: August 1, 2001

For: ANTI-JITTER CIRCUITS

Art Unit: 2816

Examiner: M.T. Nguyen.

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TECHNOLOGY CENTER 2800  
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11/2/03

**RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated July 3, 2002, please enter the following amendments and consider the following remarks.

**AMENDMENTS**

***IN THE DRAWINGS:***

Applicant herewith submits corrected formal drawings.

***IN THE SPECIFICATION:***

Please replace the paragraph beginning on page 3, line 16, through page 4, line 3, with the following:

Figures 8(a) and 9(a) show anti-jitter circuits according to the invention including circuitry arranged to maintain the charge value of charge packets substantially constant.

Figures 8(a) and 9(a) are circuit diagrams showing the anti-jitter circuits. Figures 8(b) and 9(b) show the input waveforms Op2 and the mean d.c. levels Op3 overlaid, and Figures 8(c)

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CERTIFICATE OF MAILING

I hereby certify that this RESPONSE TO OFFICE ACTION (along with any documents referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Date:

12/16/02

